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		Applicant Koen Deforche	
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C65 ↓	A	"Start-Time Fair Queueing: A Scheduling Algorithm for Integrated Services Packet Switching Networks", by Pawan Goyal, Harrick M. Vin, and Haichen Cheng. Vol. 5, No. 5, October 1997, IEEE	
	B	"A Self-Clocked Fair Queueing Scheme for Broadband Applications", by S. Jamaloddin Golestani, Bellcore, 1994 IEEE	
	C	"Analysis and Simulation of a Fair Queueing Algorithm" by Alan Demers, Srinivasan Keshav, Scott Shenker, Computer Communications Review Vol. 19, No. 4, Austin TX, September, 1989, pp.1-12	
	D	"Virtual Clock: A New Traffic Control Algorithm for Packet Switching Networks" by Lixia Zhang, 1990 ACM	
	E	"WF Q: Worst-case Fair Weighted Fair Queueing" by Jon C.R. Bennett, FORE Systems, and Hui Zhang, School of Computer Science, Carnegie Mellon University	
	F	"Implementing Scheduling Algorithms in High-Speed Networks", Donpaul C. Stephens, Jon C.R. Bennett, Hui Zhang ; sponsored by DARPA	
	G	"Hardware-Efficient Fair Queueing Architectures for High-Speed Networks" by Jennifer L. Rexford, Albert G. Greenberg and Flavio G. Bonomi	
	H	"Efficient Fair Queueing using Deficit Round Robin" by M. Shreedhar and George Varghese, SIGCOMM 1995, Cambridge, MA , ACM	
	I	"Fair scheduling with tunable latency: A round robin approach", Hemant M. Chaskar and U. Madhow. Supported by the U.S. Army Research Office under grant DAAH04-95-1-0246	
EXAMINER 		DATE CONSIDERED 3/8/07	